Refine Search

Search Results -

Terms	Documents
semiconductor and ((adjust$ or alter$) same (flatband adj voltage) same (high adj k adj dielectric))	0

Database:

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US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search: L14

[Refine Search]

[Recall Text]   [Clear]   [Interrupt]

Search History

DATE: Monday, November 21, 2005   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=TDBD; PLUR=YES; OP=ADJ		
L14	semiconductor and ((adjust$ or alter$) same (flatband adj voltage) same (high adj k adj dielectric))	0	L14
	DB=DWPI; PLUR=YES; OP=ADJ		
L13	semiconductor and ((adjust$ or alter$) same (flatband adj voltage) same (high adj k adj dielectric))	0	L13
	DB=JPAB; PLUR=YES; OP=ADJ		
L12	semiconductor and ((adjust$ or alter$) same (flatband adj voltage) same (high adj k adj dielectric))	0	L12
	DB=EPAB; PLUR=YES; OP=ADJ		
L11	semiconductor and ((adjust$ or alter$) same (flatband adj voltage) same (high adj k adj dielectric))	0	L11
	DB=USPT; PLUR=YES; OP=ADJ		
L10	semiconductor and ((adjust$ or alter$) same (flatband adj voltage) same (high adj k adj dielectric))	1	L10
	DB=PGPB; PLUR=YES; OP=ADJ		

L9   semiconductor and ((adjust$ or alter$) same (flatband adj voltage) same (high adj k adj dielectric))   3   L9

L8   semiconductor and ((adjust$ or alter$) same (flatband adj voltage) same (high adj k adj dielectric)).clms   0   L8

L7   semiconductor and (modulat$ same (flatband adj voltage) same (high adj k adj dielectric)).clms   0   L7

DB=JPAB; PLUR=YES; OP=ADJ

L6   semiconductor and (modulat$ same (flatband adj voltage) same (high adj k adj dielectric))   0   L6

DB=EPAB; PLUR=YES; OP=ADJ

L5   semiconductor and (modulat$ same (flatband adj voltage) same (high adj k adj dielectric))   0   L5

DB=USPT; PLUR=YES; OP=ADJ

L4   semiconductor and (modulat$ same (flatband adj voltage) same (high adj k adj dielectric))   0   L4

DB=PGPB; PLUR=YES; OP=ADJ

L3   semiconductor and (modulat$ same (flatband adj voltage) same (high adj k adj dielectric))   0   L3

DB=TDBD; PLUR=YES; OP=ADJ

L2   semiconductor and (modulat$ same (flatband adj voltage) same (high adj k adj dielectric))   0   L2

DB=DWPI; PLUR=YES; OP=ADJ

L1   semiconductor and (modulat$ same (flatband adj voltage) same (high adj k adj dielectric))   0   L1

END OF SEARCH HISTORY